



# Device/PLC Connection Manuals



#### **About the Device/PLC Connection Manuals**

Prior to reading these manuals and setting up your device, be sure to read the "Important: Prior to reading the Device/PLC Connection manual" information. Also, be sure to download the "Preface for Trademark Rights, List of Units Supported, How to Read Manuals and Documentation Conventions" PDF file. Furthermore, be sure to keep all manual-related data in a safe, easy-to-find location.

# 2.17 Rockwell (Allen-Bradley)

# 2.17.1 System Structure

The following describes the system structure for connecting the GP to Rockwell (Allen-Bradley) PLCs.

**The Cable Diagrams mentioned in the following tables are listed in the section titled "2.17.2 Cable Diagrams"**.

# ■ SLC 500 Series (using CPU unit Link I/F)

СРИ	Cable Diagram	GP
	<del>(</del>	
SLC-5/03	RS-232C	GP Series
SLC-5/04	(Cable Diagram 1)	ST401

# ■ PLC-5 Series (using Link I/F)

CPU	Link I/F	Cable Diagram	Target Machine
	DATA HIGHWAY PLUS	<b>+</b>	
All PLC-5	1785-KE	RS-232C	GP/GLC Series
processors which	1785-KE/C	(Cable Diagram 2)	ST401
can connect one of	1770-KF2	RS-232C	GP/GLC Series
the link unit shown		(Cable Diagram 3)	ST401
on the right.		RS-422	GP/GLC Series
		(Cable Diagram 4)	ST400

# ■ PLC-5 Series (CPU Direct Connection)

CPU *1	Cables Diagram	Target Machine
PLC-5/11	RS-232C	GP/GLC Series
PLC-5/20	(Cable Diagram 3)	OI /OLC Series
PLC-5/30		ST401
PLC-5/40		
PLC-5/40L	RS-422	GP/GLC Series
PLC-5/60	(Cable Diagram 5)	CT 400
PLC-5/60L	, ,	ST400

<sup>\*1</sup> Connect to Channel 0 (CH0).

# ■ ControlLogix 5000 Series (using CPU unit Link I/F)

CPU	Cable Diagram	GP/GLC
	<b>←</b>	
1756-L1 1756-L1M1 1756-L1M2 1756-L1M3	RS-232C <cable 6="" diagram=""></cable>	GP/GLC Series <sup>*1</sup>
1756-L55M13 1756-L55M14 1756-L55M16		ST401

<sup>\*1</sup> This PLC can be connected only to GP2000 Series, GLC2000 Series, and GP377 Series units.

# ■ MicroLogix 1000 Series (CPU Direct Connection)

CPU	Link	Cable Diagram	Target Machine
o:			
1761-L16AWA	RS-232C port on CPU	RS-232C	
1761-L32AWA 1761-L20AWA-5A	unit	<cable 7="" diagram=""></cable>	
1761-L10BWA			GP/GLC Series
1761-L16BWA			GI /GEO SCIICS
1761-L20BWA-5A			
1761-L32BWA			
1761-L10BWB			
1761-L16BWB			
1761-L20BWB-5A 1761-L32BWB			
1761-L32BVVB			ST401
1761-L32BBB			
1761-L32AAA			

# ■ MicroLogix 1200 Series (CPU Direct Connection)

CPU	Link	Cable Diagram	Target Machine
	-	*	
1762-L24AWA 1762-L24BWA 1762-L24BXB	RS-232C port on CPU unit	RS-232C <cable 7="" diagram=""></cable>	GP/GLC Series
1762-L40AWA 1762-L40B WA 1762-L40BXB			ST401

# ■ MicroLogix 1500 Series (CPU Direct Connection)

CPU	Link	Cable Diagram	Target Machine
	◀	*	
1764-LSP	RS-232C port on Base	RS-232C	GP/GLC Series
	unit	<cable 7="" diagram=""></cable>	ST401

# ■ MicroLogix 1000 Series (using Advanced Interface Converter)

CPU	Link	Cable Diagram	Target Machine
		← →	
1761-L16AWA 1761-L32AWA	Advanced Interface Converter	RS-232C <cable 8="" diagram=""></cable>	
1761-L32AWA 1761-L20AWA-5A	(1761-NET-AIC)	Cable Diagramo>	
1761-L10BWA	(170111217110)		GP/GLC Series
1761-L16BWA			GI 7020 Octios
1761-L20BWA-5A			
1761-L32BWA			
1761-L10BWB			
1761-L16BWB			
1761-L20BWB-5A			
1761-L32BWB			ST401
1761-L16BBB			
1761-L32BBB			
1761-L32AAA			

# ■ MicroLogix 1200 Series (using Advanced Interface Converter)

CPU	Link	Cable Diagram	Target Machine
		•	
1762-L24AWA 1762-L24BWA 1762-L24BXB	Advanced Interface Converter (1761-NET-AIC)	RS-232C <cable 8="" diagram=""></cable>	GP/GLC Series
1762-L40AWA 1762-L40BWA 1762-L40BXB			ST401

# ■ MicroLogix 1500 Series (using Advanced Interface Converter)

CPU	Link	Cable Diagram	Target Machine
		•	
1764-LSP	Advanced Interface Converter	RS-232C <cable 8="" diagram=""></cable>	GP/GLC Series
	(1761-NET-AIC)	Todalo 2 lag. am or	ST401

# ■ CompactLogix 5000 Series (using CPU Link I\F unit)

CPU	Link	Cable Diagram	Target Machine
o::::::	4	•	
1762-L24AWA 1762-L24BWA 1762-L24BXB	Advanced Interface Converter (1761-NET-AIC)	RS-232C <cable 8="" diagram=""></cable>	GP/GLC Series
1762-L40AWA 1762-L40BWA 1762-L40BXB			ST401

<sup>\*1</sup> Connection is possible with GP377 Series, GP77R Series, GP2000 Series and GLC2000 Series units.

# 2.17.2 Cable Diagrams

The cable diagrams illustrated below and the cable diagrams recommended by Rockwell (Allen-Bradley) may differ; however, using these cables for your PLC operations will not cause any problems.



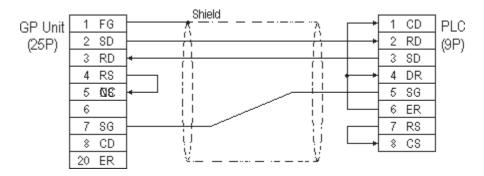
Ground your PLC's FG terminal according to your country's applicable standard. For details, refer to the corresponding PLC manual.



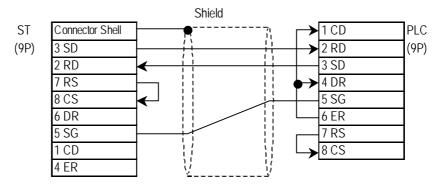
- Connect the FG line of the Shield cable to either the GP or PLC, depending on your environment. When using a connector hood and grounding the FG line, be sure to use an electrical conductor.
- For the RS-232C connection, use a cable length less than 15m.
- If a communications cable is used, it must be connected to the SG (signal ground).
- For the RS-422 connection, refer to Rockwell's PLC manual for the cable length.

#### **Cable Diagram 1**

#### **GP/GLC Series Units**

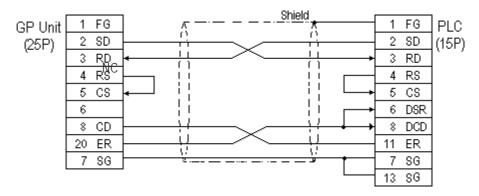


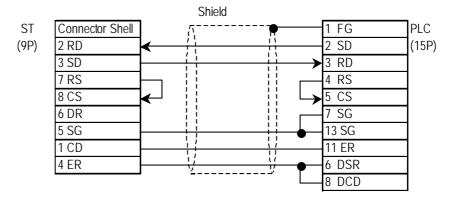
#### ST401 Unit



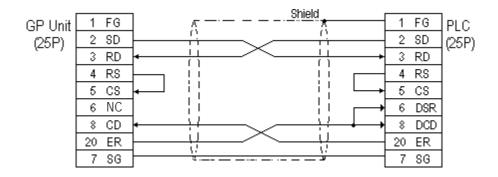
6

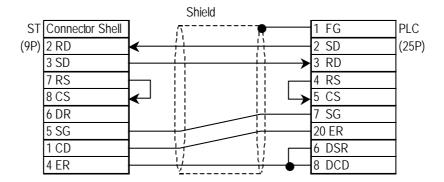
#### **GP/GLC Series Units**





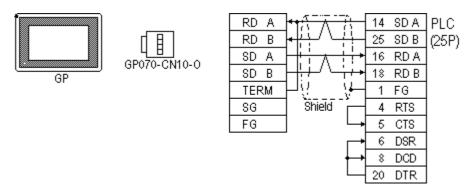
#### **GP/GLC Series Units**



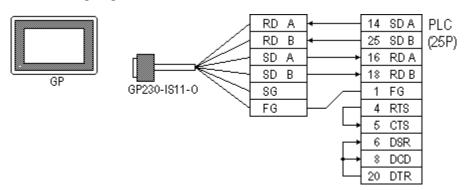


#### **GP/GLC Series Units**

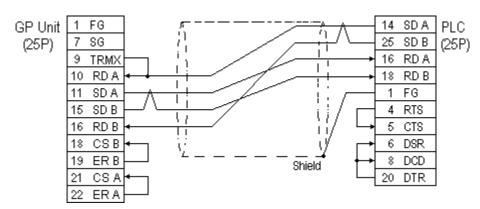
• When using Digital's RS-422 connector terminal adapter, GP070-CN10-0



• When using Digital's RS-422 Cable, GP230-IS11-0



• When making your own cable connections

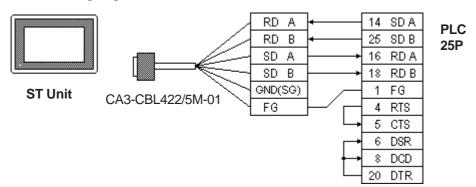




When connecting the #9 and #10 pins in the GP Serial I/F, a termination resistance of  $100\Omega$  is added between RDA and RDB.

#### ST400 Unit

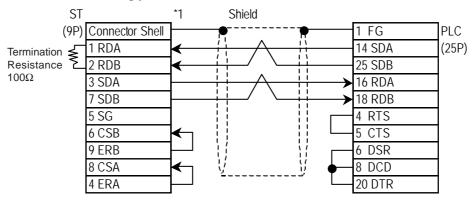
• When using Digital's RS-422 cable CA3-CBL422/5M-01





Be sure to connect the FG line to the FG terminal. For information about FG connections, refer to page 1-5 note \*1, in the "Connecting a Device/PLC to the ST unit."

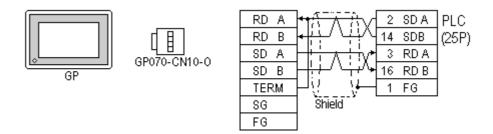
• When making your own cable connections



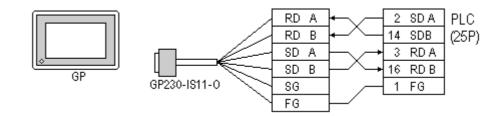
<sup>\*1</sup> Be sure to connect the shield to the Connector Shell. For information about FG connections, refer to page 1-2 "RS422 I/F (ST400)" section's Note, in the "Connecting a Device/PLC to the ST unit."

#### **GP/GLC Series Units**

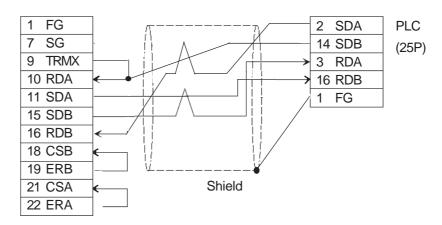
• When using Digital's RS-422 connector terminal adapter, GP070-CN10-0



• When using Digital's RS-422 Cable, GP230-IS11-0



• When making your own cable connections

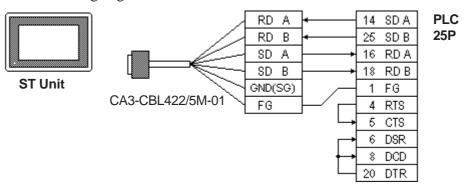




When connecting the #9 and #10 pins in the GP Serial I/F, a termination resistance of  $100\Omega$  is added between RDA and RDB.

#### ST400 Unit

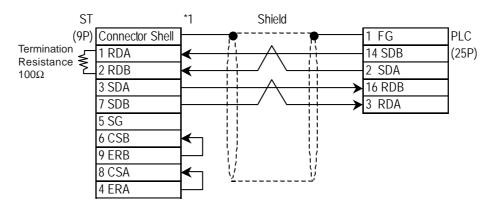
• When using Digital's RS-422 cable CA3-CBL422/5M-01





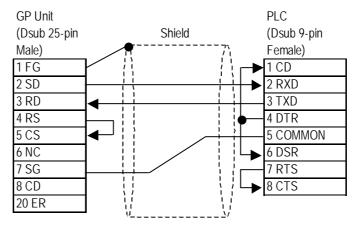
Be sure to connect the FG line to the FG terminal. For information about FG connections, refer to page 1-5 note \*1, in the "Connecting a Device/PLC to the ST unit."

• When making your own cable connections



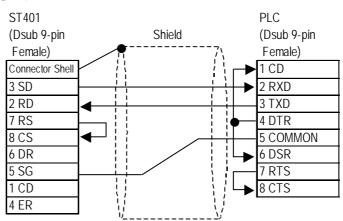
<sup>\*1</sup> Be sure to connect the shield to the Connector Shell. For information about FG connections, refer to page 1-2 "RS422 I/F (ST400)" section's Note, in the "Connecting a Device/PLC to the ST unit."

#### **GP/GLC Series Units**





- Note: Connect the shield to the GP's FG terminal.
  - If a communications cable is used, it must be connected to the SG terminal and COMMON terminal.

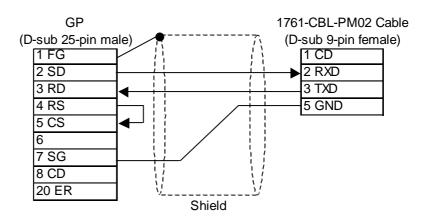


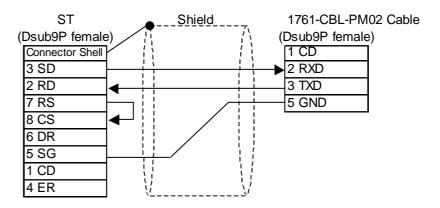


- Note: Connect the shield to the GP's FG terminal.
  - If a communications cable is used, it must be connected to the SG terminal and COMMON terminal.

# Cable Diagram 7 (RS-232C)

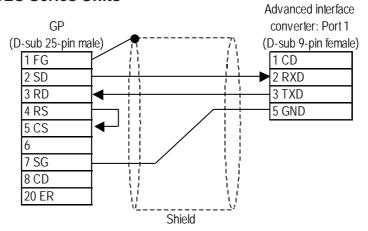
#### **GP/GLC Series Units**

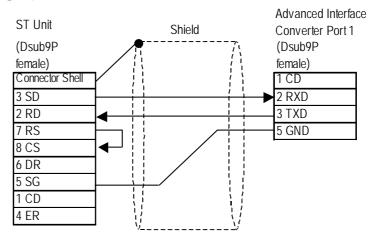




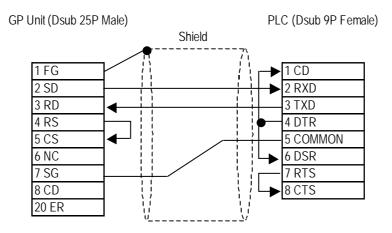
# Cable Diagram 8 (RS-232C)

#### **GP/GLC Series Units**



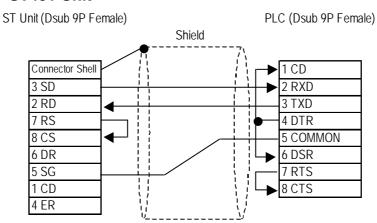


#### **GP/GLC Series Units**





- Connect the shield to the GP's FG terminal.
- If a communications cable is used, it must be connected to the SG terminal and COMMON terminal.





- Connect the shield to the GP's FG terminal.
- If a communications cable is used, it must be connected to the SG terminal and COMMON terminal.

# 2.17.3 Supported Devices

The following describes the range of devices supported by the GP.

# ■ SLC 500/MicroLogix 1000•1200•1500 Series

Setup System Area here.

Device	Bit Address	Word Address	Particular	s
Output	O:0.000/00 ~ O:63.255/15	O:0.000 ~ O:63.255	*2*3	L/H
Input	I:0.000/00 ~ I:63.255/15	I:0.000 ~ I:63.255	*2*3	L/II
Bit	B3:000/00 ~ B3:255/15	B3:000 ~ B3:255		H/L
Dit	B9:000/00 ~ B255:255/15	B9:000 ~ B255:255		11/L
Timer	T4:000/TT ~ T4:255/TT			
(TT: Timing Bit)	T9:000/TT ~ T255:255/TT			
Timer	T4:000/DN ~ T4:255/DN			
(DN: Completion Bit)	T9:000/DN ~ T255:255/DN			
Timer		T4:000.PRE ~ T4:255.PRE	*1	
(PRE: Setup Value)		T9:000.PRE ~ T255:255.PRE		
Timer		T4:000.ACC ~ T4:255.ACC	*1	
(ACC: Current Value)		T9:000.ACC ~ T255:255.ACC		
Counter	C5:000/CU ~ C5:255/CU			L/H
(CU: Up Count)	C9:000/CU ~ C255:255/CU			_,
Counter	C5:000/CD ~ C5:255/CD			
(CD: Down Count)	C9:000/CD ~ C255:255/CD			
Counter	C5:000/DN ~ C5:255/DN			
(DN: Completion Bit)	C9:000/DN ~ C255:255/DN			
Counter		C5:000.PRE~ C5:255.PRE	*1	
(PRE: Setup Value)		C9:000.PRE ~ C255:255.PRE		
Counter		C5:000.ACC ~ C5:255.ACC	*1	
(ACC: Current Value)		C9:000.ACC ~ C255:255.ACC		

continued to next page

<sup>\*1</sup> When reading and writing consecutive addresses that are all two words or longer, reading will take longer than for other devices, and the overall screen refresh speed will be slower.

<sup>\*2</sup> Not available for writes.

<sup>\*3</sup> Within the GP70 series units, this applies only to the GP377 series.

Device	Bit Address	Word Address	Particulars	S
Control	R6:000/EN ~ R6:255/EN		*3	
(EN: Enable)	R9:000/EN ~ R255:255/EN			
Control	R6:000/EU ~ R6:255/EU		*3	
(EU: Enable Unload)	R9:000/EU ~ R255:255/EU			
Control	R6:000/DN ~ R6:255/DN		*3	
(DN: Complete Bit)	R9:000/DN ~ R255:255/DN			
Control	R6:000/EM ~ R6:255/EM		*3	
(EM: Empty)	R9:000/EM ~ R255:255/EM			
Control	R6:000/ER ~ R6:255/ER		*3	
(ER: Error)	R9:000/ER ~ R255:255/ER			
Control	R6:000/UL ~ R6:255/UL		*3	
(UL: Unload)	R9:000/UL ~ R255:255/UL			
Control	R6:000/IN ~ R6:255/IN		*3	
(IN: Inhibit Comp.)	R9:000/IN ~ R255:255/IN			
Control	R6:000/FD ~ R6:255/FD		*3	
(FD: Found)	R9:000/FD ~ R255:255/FD	<del></del>		
Status	S2:000/00 ~ S2:163/15	S2:000 ~ S2:163	*2*3	
Integer		N7:000 ~ N7:255	гот 1 <b>5</b> г	H/L
meger		N9:000 ~ N255:255	Bit 1 5 1	1 1/L
Floating point		F8:000 ~ F8:255		
Floating point		F9:000 ~ F255:255		
String		ST9:000 ~ ST255:255	*3	L/H
Long word		L9:000 ~ L255:255	<u>Bit</u> 31) *3	
ASCII		A9:000 ~ A255:255	*3	

<sup>\*2</sup> Not available for writes.

<sup>\*3</sup> Within the GP70 series units, this applies only to the GP377 series.



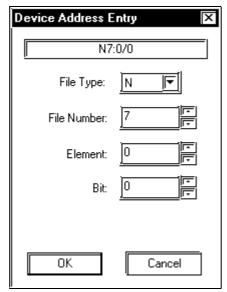
- The range of available devices depends on the type of CPU used. For available device range information, refer to your PLC's manual.
- In the above tables, the address descriptions and input methods used in GP-PRO/PBIII for Windows V6.0 or earlier software may vary, however the internal data can be converted. Even if GP-PRO/PBIII for Windows V6.0 or later software is used, the internal data will not be damaged.
- When using Version 6.0 or earlier address displays and input methods with Versions 6.1 or later software, be sure to use the following steps.
  - 1) Locate and open the folder named [SLC500] in your GP/PRO/PBIII for Windows Version 6.1 or later CD-ROM.
  - 2) Copy the file named [SLC500.TBL] to the folder [PLCTBL] on your PC's hard disk drive. (This folder was created when Version 6.1 was installed.)
  - 3) Delete the [SLC500.PTO] from the folder [PTO] was creaated when GP-PRO/PBIII was installed.
  - 4) Start up GP-PRO/PBIII for Windows. You will now be able to use Version 6.0 or earlier address displays and input methods. When running screen editor software version 6.0 or earlier, D-Script creation or screen data modification while using [SLC500.PTO] is not possible.
- File Numbers 0~8 are the User's default files.
- A PLC COM Error (02:10) develops when a device cannot be allocated into the PLC data table map.
- According to the PLC specifications, the input and output relays cannot perform direct reads and writes. As a result, perform the following procedures via the PLC:

When reading data; use a ladder program to move the input and output relay data either as bits or as integers, and then read out those bits or integers.

When writing data; write the data as either bits or integers, and then use the ladder program to move the data to the input or output relays.

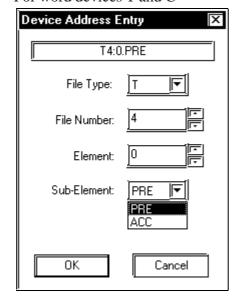
• In Rockwell (Allen-Bradley) PLCs, the structure of each device's data is determined from the Element; however, in GP-PRO/PBIII for Windows there is no Element. Therefore, use the following examples when entering device data.

• For word devices N, B, and F



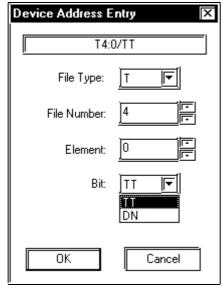
Display after input: N7:0

• For word devices T and C



Display after input: T4:0.PRE

#### • For bit devices T and C



Display after input: T4:0/TT

#### **■ PLC-5 Series**

	Setup System	Area here.
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Device	Bit Address	Word Address	Particular	S
Input Relay	100000 ~ 127717	1000 ~ 1277	<u>:8</u>	
Output Relay	O00000 ~ O27717	O000 ~ O277	<u>:8</u>	H/L
Internal Relay	B300000 ~ B6799915	B3000 ~ B67999		
Timer (TT: Timing Bit)	TT3000 ~ TT67999			
Timer (TD: Complete Bit)	TD3000 ~ TD67999			
Counter (CC: Count)	CC3000 ~ CC67999			
Counter (CD: Complete Bit)	CD3000 ~ CD67999			
Timer (ACC: Current Value)		TA3000 ~ TA67999		L/H
Timer (PRE: Setup Value)		TP3000 ~ TP67999		
Counter (ACC: Current Value)		CA3000 ~ CA67999		
Counter (PRE: Setup Value)		CP3000 ~ CP67999		
Data Register Integer		N3000 ~ N67999	Bit 1 51	
Data Register BCD		D3000 ~ D67999	Bit 1 51	H/L
Data Register ASCII		A3000 ~ A67999	Bit 1 5 1	



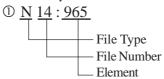
- The range of supported devices may be different depending on your CPU.
- In Rockwell (Allen-Bradley) PLCs, the structure of each device data is determined from the *Element*; in GP-PRO/PBIII for Windows there is no concept called the *Element*. Use the following examples when entering device data.

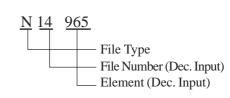
#### **PLC Data**

#### GP-PRO/PBIII Input

■When Using Elements

✓ Element delimiter

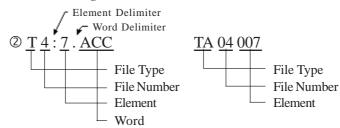




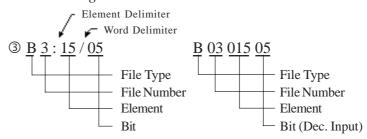
#### **PLC Side**

#### **GP-PRO/PBIII Input**

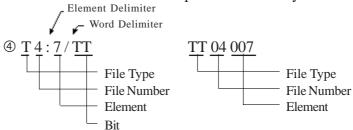
#### ■When Using Words



#### ■When Using Bits



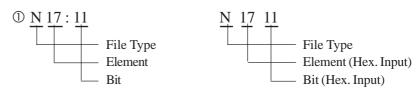
B301505 represents the same data as B3/245 (number 245 bits of file number 3). However, GP-PRO/PBIII for Windows cannot be used to input B3/245 directly.



• There is no File Number for the *Input Relay* and *Output Relay*. Also, the Element and Bit Numbers are Hexadecimal.

#### **PLC Side**

#### **GP-PRO/PBIII Input**



# ■ ControlLogix 5000 Series/CompactLogix 5000 Series

	Setup System	Area	here.
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Device	Bit Address	Word Address	Particulars	
Bit (BOOL)	BOOL00000000~BOOL99999931	BOOL000000~BOOL999999	*1*3	
8 bit integer (SINT)		SINT000000~SINT999998	<u>Bit 7) (÷2</u> 1	L/H
16 bit integer (INT)		INT000000~INT999999	Bit ] 5] *1	L/11
32 bit integer (DINT)		DINT000000~DINT999999	<u>Bit 31)</u> *1	
32 bit float (REAL)		REAL000000~REAL999999	*1*2	H/L

\*1 When using a GP Series unit to access a ControlLogix 5000/Compact Logix 5000 Series unit's data memory, you must first allocate data memory's array elements. When allocating array elements, use File numbers and Element numbers. An address designation example is shown below for GP-PRO/PBIII for Windows.

- \*2 This device uses Float. When this type of device is used, only the E-tag and K-tag's "32-bit Float" setting can be used.
- \*3 The BOOL device descriptions used in the GP-PRO/PBIII manual and the RSLogix 5000 manual are different. Please be aware of these differences when setting up BOOL devices.

GP-PRO/PBIII manual	000000 00~ 000000 31	000001 00~ 000001 31	000002 00~ 000002 31	ı	000999 00~ 000999 31
RSLogix 5000 manual	0~31	32~63	64~95	1	31968~31999



When using the GP to access a PLC device, be sure to first allocate all Tags used by PLC Data Memory to their respective devices. Use the Rockwell's RSLogix 5000 ladder logic software to allocate these devices.

If device allocation is not performed, a Host Communication Error will occur (02:D6).

**▼Reference** 2.17.5 Error Codes

#### **Device Setting Example**

The following explanation is for the ControlLogix 5000/CompactLogix 5000 device allocation.

#### 1) PLC Tag Settings

Designate the Tag Name and Type.

- Tag Name : Can be set to any value. (Not related to GP device name)

- Type : Use one of the following data types for the Element setting.

(Use the same device name as the GP)

BOOL (32-bit data type)

INT (word data type)

DINT (dword data type)

SINT (byte data type)

REAL (float data type)

#### Example 1

Tag Name	Туре
N7	INT[200]
DINT1	DINT[100]
DATA2	SINT[50]

This example's data uses the following values.

Row1 : Tag Name "N7" uses the INT data type for a 200 element array.

Row2 : Tag Name "DINT1" uses the DINT data type for a 100 element array.

Row3 : Tag Name "DATA2" uses the SINT data type for a 50 element array.

Be sure to set the number of array elements within the GP unit's maximum usable range. (The GP can access up to 999 elements.)

Also, if array elements are not designated, only one element can be used.

Ex. Tag Name:N8, Type:INT allows only one word to be used by N8.

#### 2) Mapping Settings

The tag name set in 1) can have any desired file number allocated to it. It is not possible to set the same file number to two tag names.

#### Example 2

File Number	Tag Number	
2	DATA2	
1	DINT1	
7	N7	

# 2.17.4 Environment Setup

The following tables list Digital's recommended PLC and GP communication settings.

#### ■ SLC 500 Series

GP Setup		Special Interface Module Setup		
Baud Rate	19200 bps	Baud Rate 19200 bps		
Data Length	8 bits (fixed)			
Stop Bit	1 bit (fix ed)			
Parity Bit	EVEN	Parity Bit	EVEN	
Data Flow Control	ER Control			
Communication Format	RS-232C			
DF1Mode <sup>*3</sup>	HALF / BCC	Communication Driver	DF1 HALF-DUPLEX SLAVE	
(HALF-DUPLEX)		Error Detection	BCC	
DF1Mode <sup>*3</sup> (FULL-DUPLEX)	FULL / CRC	Communication Driver	DF1 FULL-DUPLEX SLAVE	
(I ULL-DUFLEX)		Error Detection	CRC	
		Duplicate Packet Detection	DISABLE *1	
		Control Line	No Handshaking *1	
Unit No. (DH GP) *2	0	Station Address *2	0	
Unit No. (DH PLC)*2	0			

<sup>\* 1</sup> Will not operate with any other settings.

**TReference** Specifying DH addresses

<sup>\* 2</sup> Setup the Station Address and the GP's Unit No. (DH GP) address to the same value (address set as decimal values). It is unnecessary to setup the DH PLC address.

<sup>\* 3</sup> With the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode.

#### **■ PLC-5 Series**

GP Setup		CPU (CH0), 178	CPU (CH0), 1785-KE, 1770-KF2	
Baud Rate	19200 bps	Baud Rate	19200 bps	
Data Length	8 bits (fixed)	Data Length	8 bits (fixed)	
Stop Bit	1 bit (fixed)	Stop Bit	1 bit (fixed)	
Parity Bit	EVEN	Parity Bit	EVEN	
Data Flow Control	ER Control		-	
Communication Format (RS-232C)	RS-232C	Communication Format (RS-232C)	RS-232C	
Communication Format (RS-422)	4-wire type	Communication Format (RS-422)	RS-422A	
DF1Mode*6	HALF / BCC	Comm. protocol	Half duplex (DF1 Slave for CH0)	
(HALF-DUPLEX)		Error Check	BCC	
DF1Mode <sup>*6</sup>	FULL / CRC	Comm. protocol	Full duplex (DF1 Slave for CH0)	
(FULL-DUPLEX)		Error Check	CRC	
		Duplicate Detect	OFF *1	
		Control Line	NO HANDSHAKING *1	
		Other CH0 Parameters	50	
		DF1 retries	3	
		Diag file	0 (unused file)	
		RTS send delay	0	
		RTS off delay	0	
		Network link *2	Data Highway Plus	
Unit No. (DH GP) *3	0	Station Address *4 *5 (1785-KE, 1770-KF2 side)	0	

<sup>\*1</sup> Will not operate with any other settings.

#### **TReference** Specifying DH addresses

<sup>\*6</sup> with the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode.



When using CH0, setup the CPU to Slave. Do not setup as Point to Point.

<sup>\*2</sup> This is the KF2 setup

<sup>\*3</sup> Set the DH GP to station address 1785-KE or 1770-KF2, and set the DH PLC's to the CPU's station address. When using the 1785-KE or 1770-KF2, enter different numbers for the DH GP and DH PLC addresses. With a direct CPU connection, enter the same values in the DH GP and DH PLC addresses. With the GP in Offline Mode, use the "Operating Environment Setup" area to enter the DH address (DH, GP, DH, PLC) base 10 (decimal) values.

<sup>\*4</sup> When using the programming unit, make sure the Terminal Address (programming equipment address) and the Station Address do not overlap.

<sup>\*5</sup> Unavailable for CPU Direct Connection.

# **■** ControlLogix 5000 Series

GP Setup		PLC	Setup
Baud Rate	19200 bps	Baud Rate *1	19200 bps
Data Length	8 bit	Data Bits *1	8 bit
Stop Bit	1 bit	Stop Bit *1	1 bit
Parity Bit	Even	Parity *1	Even
Data Flow Control	ER		
Communication Format	Communication Format RS-232C		
Unit No. 0		Station Address *2	0
			System
			No Handshake
		RTS Send Delay *1	0
		RTS Off Delay *1	0
DF1Mode <sup>*3</sup>	HALF / BCC	Protocol *2	DF1Half duplex slave
(HALF-DUPLEX)	HALF / DCC	Error Detection *2	BCC
DF1Mode <sup>*3</sup>	FULL / CRC	Protocol *2	DF1Full duplex slave
(FULL-DUPLEX)	FULL / CRC	Error Detection *2	CRC
		Transmit Retries *2	3
		Slave Poll Timeout *2	3000

<sup>\*1</sup> Set via the Rockwell Ladder Logic Software RSLogix 5000 "Serial Port" menu.

# ■ MicroLogix 1000 Series (CPU Direct Connection)

GP Setup		PLC Setup		
Baud Rate	19200bps	Baud Rate	19200bps	
Data Length	8bits	-	-	
Stop Bit	1bit	-	-	
Parity Bit	Non	Parity	Non	
Control method	ER Control	-	-	
Communication Format	RS-232C	-	-	
(RS-232C)	RS-422(4-wire type)	-	-	
DH Address GP	0 to 254	Nada Addraga	0 to 254	
DH Address PLC *1	0 to 254	Node Address	0 to 254	
DF1Mode <sup>*2</sup>	Half/BCC	Driver	DF1 Half Duplex Slave	
(HALF-DUPLEX)	пашьсс	Error Detection	BCC	
DF1Mode <sup>*2</sup>	Full/CRC	Driver	DF1 Full Duplex Slave	
(FULL-DUPLEX)	Full/CRC	Error Detection	CRC	
-	-	Control Line	No Handshaking	
-	-	EOT Suppression	No check	
-	-	Duplicate Packet Detect	No check	
-		Poll Timeout	3000	
-	-	Message Retries	3	
-	-	Pre Transmit Delay	0	

<sup>\*1</sup> Specify the same address for DH Node Address GP and DH Node Address PLC.

<sup>\*2</sup> Set via the Rockwell Ladder Logic Software RSLogix 5000 "System Protocol" menu

<sup>\*3</sup> with the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode..

<sup>\*2</sup> with the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode.

# ■ MicroLogix 1200/1500 Series (CPU Direct Connection)

GP Setup		PLC Setup	
Baud Rate	19200bps	Baud Rate	19200bps
Data Length	8bit	-	-
Stop Bit	1bit	-	-
Parity Bit	EVEN	Parity	Even
Control Method	ER Control	-	-
Communication Format	RS-232C	-	-
	RS-422(4-wire type)	-	-
DH Address GP	0 to 254	Node Address	0 to 254
DH Address PLC *1	0 10 254	Node Address	0 10 254
Communication Format	RS-232C	-	-
DF1Mode <sup>*2</sup> (HALF-DUPLEX)	Half/BCC	Driv er	DF1 Half Duplex Slave
		Error Detection	BCC
DF1Mode <sup>*2</sup> (FULL-DUPLEX)	Full/CRC	Driv er	DF1 Full Duplex Slave
		Error Detection	CRC
-	-	Control Line	No Handshaking
-	-	EOT Suppression	OFF
-	-	Duplicate Packet Detect	OFF
-	-	Poll Timeout	3000
-	-	Message Retries	3
-	-	Pre Transmit Delay	0

<sup>\*1</sup> Specify the same address for DH Node Address GP and DH Node Address PLC.

# ■ MicroLogix 1000/1200/1500 Series (Using Advanced Interface Converter)

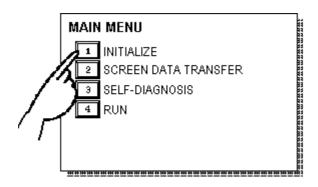
GP Setup		PLC Setup	
Baud Rate	19200 bps	Baud Rate	Auto
Data Length	8 bits		
Stop Bit	1 bit	1 bit — — —	
Parity Bit	Non		
Control Method	ER Control		
Communication Format	RS-232C		
	RS-422 (4-wire type)		
DH Address GP	0 to 254		
DH Address PLC *1	0 10 234		

<sup>\*1</sup> Specify the same address for DH Node Address GP and DH Node Address PLC.

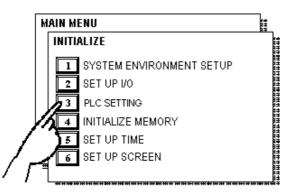
<sup>\*2</sup> with the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode

# **■** Specifying DH addresses

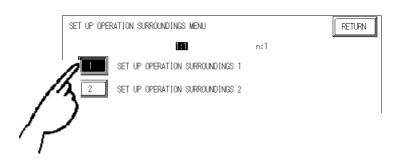
Set up the operating environment at the initial setup when the GP is in the OFFLINE mode.



Touch item #1, INITIALIZE. The INITIALIZE menu will appear.



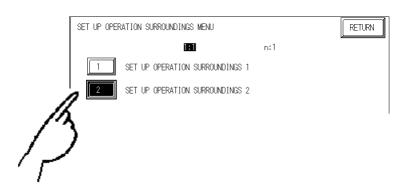
② Touch item #3, PLC SETTING. The PLC SETTING menu will appear.



③ Touch selection [1:1] and then item #1, SET UP OPERATION SURROUNDINGS 1. The selected option is thenhighlighted.



④ Specify the DH addresses. Then, touch [SET].



⑤ Touch item #2, SET UP OPERATION SURROUNDINGS 2.

Within the GP70 series units, this applies only to the GP377 series.



Specify [Error Detection] and [DF1 mode]. Then, touch [SET].

# **■** CompactLogix 5000 Series

GP Setup		PLC Setup	
Baud Rate	19200bps *1	Baud Rate *3	19200 bps
Data Length	8 bits	Data Bits *3	8 bits
Stop Bit	1 bit	Stop Bit *3	1 bit
Parity Bit	Even	Parity *3	Even
Data Flow Control	ER	-	-
Communication Format	R S - 232C	-	-
Unit No.	0 *2	Station Address *4	0
-	•	Mode *3	System
- - -		Control Line *3	No Handshake
		RTS Send Delay *3	0
		RTS Off Delay *3	0
DF1Mode <sup>*5</sup> (HALF-DUPLEX)	H a lf/B C C	Protocol*4	DF1 Half Duplex Slave
		Error Detection	BCC
DF1Mode <sup>*5</sup> (FULL-DUPLEX)	Full/CRC	Protocol*4	DF1 Full Duplex Slave
		Error Detection	CRC
-		Transmit Retries *4	3
-		Slave Poll Timeout *4	3000
-		EOT Suppression *4	No Check
		Enable Duplicate Detection *4	No Check (Disable)

<sup>\*1</sup> Data communication can be performed at 38400bps.

<sup>\*2</sup> Unit numbers can be set from 0 to 254.

<sup>\*3</sup> Set via the Rockwell Ladder Logic Software RSLogix 5000 "Serial Port" menu.

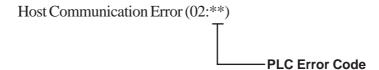
<sup>\*4</sup> Set via the Rockwell Ladder Logic Software RSLogix 5000 "System Protocol" menu.

<sup>\*5</sup> with the GP in offline mode, use the "Operrating Environment Setup" area to set the DF1 mode.

# 2.17.5 Error Codes

#### **■PLC Error Codes**

Controller error codes are represented by the "Host communication error (02:\*\*)", and indicated in the left lower corner of the GP screen. (\*\* stands for an error code.)



\* There are two types of PLC error codes - STS and EXT STS.

EXT STS error codes have the characters "0xD0" attached to them, to prevent them from overlapping with STS error codes. Thus, all error codes with the last characters of "0xCF" or earlier are STS error codes.

Ex.

When a (02:D2) Host Communication Error occurs, it becomes the EXT STS error code of "0x02".

When a (02:C0) Host Communication Error occurs, it becomes the STS error code of "0xC0".