## **Pro-face**



# Device/PLC Connection Manuals



#### About the Device/PLC Connection Manuals

Prior to reading these manuals and setting up your device, be sure to read the "Important: Prior to reading the Device/PLC Connection manual" information. Also, be sure to download the "Preface for Trademark Rights, List of Units Supported, How to Read Manuals and Documentation Conventions" PDF file. Furthermore, be sure to keep all manual-related data in a safe, easy-to-find location.

### Fuji Electric - Appendix

#### **Maximum Number of Consecutive Device Addresses A.1**

The following lists the maximum number of consecutive addresses that can be read by each PLC. Refer to these tables to utilize *Block Transfer*.



**Note:** When the device is setup using the methods below, the Data Communication Speed declines by the number of times the device is read.

- When consecutive addresses exceed the maximum data number range
- When an address is designated for division
- When device types are different

To speed up data communication, plan the tag layout in screen units, as consecutive devices. (Includes the Alarm and Trend screens.)

#### **PLC**

<MICREX-F Series>

Device	Max. No. of Consecutive Addresses	Device	Max. No. of Consecutive Addresses
Input/Output Relay B		Timer 0.1 (current	
mpur o'upur Kelay D		value) W9	
Auxilary Relay M		Counter (current	
	10.11	value) C R	
Keep Relay K	48 Words	Coutner (setup	24 Words
Reep Reidy R		value) C S	
Differential Relay D		Data Memory BD	
Link Relay L		Data Memory DI	
Timer (0.01 sec) T		Data Memory SI	
Timer (0.1 sec) T	1 Word	File Memory (W30)	48 Words
Counter C		File Memory (W31)	
Direct Input/Output W	48 Words	File Memory (W32)	
Timer 0.01 (current		File Memory (W33)	
value) TR	24 Words		24 Words
Timer 0.01 (setup	21 110103	File Memory (W34)	21 10003
value) TS		The memory (W34)	

#### Appendix

<MICREX-SX Series>

Device	Max. No. of Consecutive Addresses
Input Memory	
Output Memory	
Standard Memory	243 Words
Retain Memory	
System Memory	

#### <FLEX-PC N Series>

Device	Max. No. of Consecutive Addresses	Device	Max. No. of Consecutive Addresses
Input Relay X		Data Register D	
Output Relay Y		Special Register D	
Internal Relay M		Link Register W	
Extended Internal Relay M	105 Words	File Register R	
Latch Relay L		Timer (current value) T	105 Words
Extended Latch Relay L		Timer (setup value) TS	
Special Relay M		Counter (current value) C	
Timer T		Coutner (setup value) CS	
Counter C			

#### Inverters

<Micro-Controller X Series (Model:PXR)>

Device	Max. No. of Consecutive Addresses	
Standard Feature	1 bit	
Terminal Feature	8 bit	
Control Feature	15 words	
Motor 1	60 words	
High-Level Feature	15 words	
Motor 2	60 words	

<FRENICS5000G11S, FRENICS5000P11S, FVR-E11S, FVR-C11S Series>

Device	Max. No. of Consecutive Addresses				
Standard Feature					
Terminal Feature					
Control Feature					
Motor 1					
High-Level Feature	1 Word				
Motor 2					
Option					
Instruction Data					
Monitor Data					

GP-PRO/PBIII for Windows Device/PLC Connection Manual

#### A.2 Device Codes and Address Codes

Device codes and address codes are used to specify indirect addresses for the E-tags or K-tags.

The word addresses of data to be displayed are coded and stored in the word address specified by the E-tags and K-tags. (Code storage is done either by the PLC, or with T-tag and K-tags)

#### **PLC**

<MICREX-F Series>

		Word	Device		
	Device	Address	Code	Address Code	
			(HEX)		
	Input Relay	WB0000~	8040	Word Address	
	Direct I/O	W24.0000~	4840	Word Address	
	Auxilary Relay	WM0000~	9040	Word Address	
Bit	Keep Relay	WK000~	C040	Word Address	
Device	Differential Relay	WD000~	D040	Word Address	
	Link Relay	WL000~	C840	Word Address	
	Special Relay	WF0000~	B040	Word Address	
	Announce Relay	WA0000~	B840	Word Address	
	Timer 0.01 sec	T R0000~	6080	Word Address	
	(current value)	1 K0000~	0000	word Address	
	Timer 0.01 sec	T S0000~	6880	Word Address	
	(set value)	1 30000~		WORU AUDIESS	
	Timer 0.1 sec	W9.000~	6480	Word Address	
	(current value)	VV9.000 <sup>12</sup>			
	Counter (curent	CR0000~	7080	Word Address	
	value)		,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
Word	Counter (set	CS0000~	7880	Word Address	
Device	value)				
		BD0000~	0080	Word Address	
	Data Memory	D10000~	0880	Word Address	
		S10000~	0440	Word Address	
		W30.0000~	2040	Word Address	
		W31.0000~	2240	Word Address	
	File Memory	W32.0000~	2440	Word Address	
		W33.0000~	2680	Word Address	
		W34.0000~	2880	Word Address	
	LS Area	LS0000~	4040	Word Address	

Appendix

Device	Word Address	Device Code	Address Code
Input Memory	%IW1.0 ~	0x8000	Word Address
Output Memory	%QW1.0 ~	0x8800	Word Address
	%MW1.0~	0x9000	Word Address
Γ	%MW 1.65536 ~	0x9200	Word Address
Γ	%MW 1.131072 ~	0xD000	Word Address
Γ	%MW 1.196608 ~	0xD200	Word Address
Γ	%MW1.1.0~	0x9400	Word Address
Γ	%MW 1.1.65536 ~	0x9600	Word Address
Γ	%MW 1.1. 131072 ~	0xD400	Word Address
Γ	%MW 1.1. 196608 ~	0xD600	Word Address
Γ	%MW 2.1.0 ~	0x9800	Word Address
Standard Memory	%MW 2.1.65536 ~	0x9A00	Word Address
Standard Memory	%MW 2.1. 131072 ~	0xD800	Word Address
Γ	%MW 2.1.196608 ~	0xDA00	Word Address
Γ	%MW 3.1.0 ~	0x9C00	Word Address
Γ	%MW 3.1.65536 ~	0x9E00	Word Address
Γ	%MW 3.1. 131072 ~	0xDC00	Word Address
Γ	%MW 3.1.196608 ~	0xDE00	Word Address
Γ	%MW 4.1.0 ~	0xA000	Word Address
	%MW 4.1.65536 ~	0xA200	Word Address
Γ	%MW 4.1. 131072 ~	0xE000	Word Address
Γ	%MW 4.1. 196608 ~	0xE200	Word Address
Retain Memory	%MW 3.0 ~	0xB000	Word Address
	%MW 3.65536 ~	0xF000	Word Address
Γ	%MW 3. 131072 ~	0x8400	Word Address
	%MW 3. 196608 ~	0x8200	Word Address
Γ	%MW1.3.0~	0xB200	Word Address
	%MW 1.3.65536 ~	0xF200	Word Address
	%MW 1.3. 131072 ~	0x8600	Word Address
	%MW 1.3. 196608 ~	0xAC00	Word Address
	%MW 2.3.0 ~	0xB400	Word Address
	%MW 2.3.65536 ~	0xF400	Word Address
	%MW 2.3. 131072 ~	0x8A00	Word Address
	%MW 2.3.196608 ~	0xAE00	Word Address
	%MW 3.3.0 ~	0xB600	Word Address
	%MW 3.3.65536 ~	0xF600	Word Address
	%MW 3.3. 131072 ~	0x8C00	Word Address
	%MW 3.3. 196608 ~	0xEC00	Word Address
L T	%MW 4.3.0 ~	0xB800	Word Address
l T	%MW 4.3.65536 ~	0xF800	Word Address
L T	%MW 4.3. 131072 ~	0x8E00	Word Address
<b>_</b>	%MW 4.3.196608 ~	0xEE00	Word Address

<MICREX-SX Series>

System Memory	%MW 1.0 ~	0xC000	Word Address
	%MW 1.10.0 ~	0xC200	Word Address
	%MW 2.10.0 ~	0xC400	Word Address
	%MW 3.10.0 ~	0xC600	Word Address
	%MW 4.10.0 ~	0xC800	Word Address
LS Area	LS0000 ~	0x4000	Word Address

#### <FLEX-PC Series>

	Device	Word Address	Device Code (HEX)	Address Code
	Input Relay	WX000~	8040	Word Address
	Output Relay	WY000~	8840	Word Address
	Internal Relay	WM000~	9040	Word Address
Bit Device	Extended Internal Relay	WM040~	9840	Word Address
	Latch Relay	WL000~	C 040	Word Address
	Extended Latch Relay	WL040~	C 840	Word Address
	Special Relay	WM 800~	Х	Х
	Timer (current value)	T0000~	6000	Word Address
	Timer (set value)	TS0000~	6800	Word Address
	Counter (current value)	C 0000~	7000	Word Address
Mond	Counter (set value)	C \$0000~	7800	Word Address
Word Device	Data Register	D0000~	0040	Word Address
	Special Register	D8000~	Х	Х
	Link Register	W0000~	0440	Word Address
	File Register	R0000~	4840	Word Address
	LS area	LS0000~	4040	Word Address

#### Inverters

<Micro-Controller X Series (Model:PXR)>

	Device	Word Address	Device Code (HEX)	Address Code
Bit Device		00001 ~	8000	Cannot be set
DIEDEVICE		10001 ~	8200	Word Address minus 1
	Parameter vice	30001 ~	8400	Word Address minus 1
		40001 ~	8600	Word Address minus 1
Word Device		31001 ~	8800	Word Address minus 1
		41001 ~	8A00	Word Address minus 1
	LS Area	LS0000 ~	4000	Word Address

#### <FRENICS5000G11S, FRENICS5000P11S, FVR-E11S, FVR-C11S Series>

	Device	Word Address	Device Code (HEX)	Address Code
	Standard Feature	F00 ~	0	Word Address
	Terminal Feature	E01 ~	1000	Word Address minus 1
	Control Feature	C01 ~	2000	Word Address minus 1
	Motor 1	P00 ~	3000	Word Address
Word	High-level Feature	H01 ~	5000	Word Address minus 1
Device	Motor 2	A01 ~	6000	Word Address minus 1
	Option	000 ~	7000	Word Address
	Instruction Data	S01 ~	1200	Word Address minus 1
	Monitor Data	M01 ~	1400	Word Address minus 1
	Alarm Reset	m00 ~	1600	Word Address
	LS Area	LS0000 ~	4000	Word Address