Melsec-Q CPU (SIO) Driver

- 1 System Structure
- 2 Cable Diagrams
- 3 Supported Device Addresses
- 4 Consecutive Device Addresses
- 5 Environment Setup
- 6 I/O Manager Configuration
- 7 Driver Configuration
- 8 Device Address Configuration

• This manual explains how to connect the target machine with other manufacturer devices. For information about how to use the Pro-Designer software, please refer to the Pro-Designer Online Help.

• The types of target machines that are compatible with Pro-Designer depends on the version of Pro-Designer. For information about the compatibility of target machines, please refer to the Pro-Designer Online Help.

© 2002 Digital Electronics Corporation. All rights reserved.

1 System Structure

The following table describes the basic system setup for connecting the target machine to Mitsubishi Melsec-Q Series PLCs over a serial connection.

To view a cable connection diagram for a particular communication format, see Section 2 – *Cable Diagrams*.

Series	CPU ^{*1}	Link I/F	Comm. Format	Diagram
MELSEC Q-Series	QO2CPU Q02HCPU Q06HCPU Q12HCPU Q25HCPU	CPU Direct	RS-232C	Cable Diagram 1

*1 Connect to the CPU's RS-232C port.

2 Cable Diagrams

The illustrated cable diagrams and those recommended by Mitsubishi may differ. However, Proface recommends using the following diagrammed connections.

Ground the PLC's FG terminal according to your country's applicable standard. For details, refer to the PLC manual.

Cable Diagram 1 RS-232C

To connect the target machine and the PLC, use a recommended cable or create your own cable using the following specifications.

Target Machine	Cable / Adapter	Comments
GP, PS-P, PC/AT(PL), PS-G	RS-232C Cable (Mitsubishi: QC30R2 (3m) OR Diatrend: DQCABR2*-H (*m))	





МЕМО

- Attaching a ferrite core will reduce the amount of noise in the cable (see the following diagrams.)
- Attach two ferrite cores to the cable, one at each end. Loop the cable once around each ferrite core.



Recommended Ferrite Core Maker :Seiwa Electronics Corporation Model :E04SR301334



You can use ferrite cores from other manufacturers, but make sure they are the same size as defined here.

Conversion Adaptor Specifications

- Straight connection type
- Dsub 25pin male Lock-screw (mm)
- Dsub 9pin male Lock nut (inch)

<Adaptor : Roas Co. Model No. ZA-403>



3 Supported Device Addresses

The following table lists the device address ranges you can enter from the *Device Address Keypad*.

For actual device address ranges supported by the PLC, refer to the corresponding PLC manual. Supported device addresses differ from protocol to protocol and between PLC models.

Device	Bit Address	Word Address	16 bit	32 bit
Input Relay	X0000-XFFFF	X0000-XFFF0*1		
Output Relay	Y0000-YFFFF	Y0000-YFFF0*1		
Internal Relay	M00000-M65535	M00000-M65520*2		
Latch Relay	L00000-L65535	L00000-L65520*2		
Special Relay	SM0000-SM9999	SM0000-SM9984*2		
Annunciator	F00000-F65535	F00000-F65520*2		
Edge Relay	V00000-V65535	V00000-V65520*2		
Step Relay	S0000-S9999	S0000-S9984*2		
Link Relay	B0000-BFFFF	B0000-BFFF0*3		
Special Link Relay	SB000-SBFFF	SB000-SBFF0*3		
Timer (contact)	TS00000-TS65535			
Timer (coil)	TC00000-TC65535			
Retentive Timer (contact)	SS00000-SS65535			
Retentive Timer (coil)	SC00000-SC65535			
Counter (contact)	CS00000-CS65535		і 1⊔*6	ı ⊔*6
Counter (coil)	CC00000-CC65535			L11
Timer (current value)		TN00000-TN65535		
Retentive Timer (current value)		SN00000-SN65535		
Counter (current value)		CN00000-CN65535		
Data Register ^{*3}	D00000:0-D65535:15	D00000-D65535*4		
Special Register*3	SD0000:0-SD9999:15	SD0000-SD9999*4		
Link Register*3	W0000:0-WFFFF:F	W0000-WFFF ^{*5}		
Special Link Register*3	SW000:0-SWFFF:F	SW000-SWFFF ^{*5}		
File Register(Normal)*3	R00000:0-R65535:15	R00000-R65535*4		
	0R0000:0-0R7FFF:F	0R0000-0R7FFF ^{*5}		
	1R0000:0-1R7FFF:F	1R0000-1R7FFF ⁵		
File Register $(0P_{-}31R)^{*3}$	2R0000:0-2R7FFF:F	2R0000-2R7FFF ^{*5}		
	30R0000:0-30R7FFF:F	30R0000-30R7FFF ^{*5}		
	31R0000:0-31R7FFF:F	31R0000-31R7FFF ^{*5}		

*1 To use as word address, the bit column (last column) must be 0.

- *2 To use as word address, must be multiple of 16.
- *3 When the bit write operation is performed, the Target Machine reads the PLC's corresponding word address and turns a bit ON, then send back to PLC. Do not write to the word address from the ladder program in the middle of this operation.
- *4 You can define a bit address by adding a colon followed by the bit position (0~15) at the end of the word. (e.g. D0100:8)
- *5 You can define a bit address by adding a colon followed by the bit position (0~F) at the end

of the word. (e.g. W0001:A) *6 16-bit and 32-bit data, High and Low, refer to data as defined in the following examples.

Byte		16 bit			Word		32 bit		_
0	7		0	L (Low)	0	15		0	L (Low)
1	15		8	H (High)	1	31		16	H (High)
					L				-

4 Consecutive Device Addresses

The following table lists the maximum number of consecutive addresses that can be read by each PLC. Refer to this table when using block transfers.



- To speed up data communication, use consecutive device addresses on the same panel screen.
- The following situations increase the number of times that the device is read, and reduces the data communication speed between the target machine and the PLC:
 - when the number of consecutive addresses exceeds the maximum
 - when an address is designated for division
 - when different device types are used

Device	Max. Consecutive Addresses	Gap Span
Input Relay (X)		
Output Relay (Y)		
Internal Relay (M)		
Latch Relay (L)		
Link Relay (B)	15360 bits	1535 bits
Timer [contact] (TS)		
Timer [coil] (TC)		
Counter [contact] (CS)		
Counter [coil] (CC)		
Data Register (D)		
Link Register (W)		
File Register (R)	060 words	06 words
Extended File Register (R)	900 Wolds	90 WOIds
Timer [current value] (TN)]	
Counter [current value] (CN)		

5 Environment Setup

The following table lists the communication settings, recommended by Pro-face, for the target machine and Mitsubishi MELSEC PLCs.

For details, see Section 7 – *Driver Configuration*.

	Target Machi	PLC Settings	
	Serial Interface	RS-232C	
	Flow Control	None	
	Transmission Speed	19200 bps	
Retry Count Driver Parity Bit Stop Bit Data Length Rcv. Timeout TX Wait Time	2		
	Parity Bit	Odd	
	Stop Bit	1 bit	
	Data Length	8 bits	
	Rcv. Timeout	10 sec	
	TX Wait Time	0 msec	

6 I/O Manager Configuration

The driver and protocol, which enable communication between the target machine and the PLC, depends on the PLC type.

MEMO

For information on how to display the [New Driver Interface] dialog box, see the online help.

lew Driver Interface		
Mitsubishi Electric Corp.		-
Driver:	Protocol:	
Melsec-A CPU (SIO) Melsec-A Link (SIO) Melsec-FX (CPU) Melsec-FX (CPU) Melsec-FX Link (SIO) Melsec-Q CPU (SIO) Melsec-Q Ethernet(TCP) Melsec-Q Ethernet(UDP) Melsec-Q Link(SIO)	Q(Cpu)	
	OK Cancel	

Driver Configuration 7

To configure the communication settings of the serial driver in the target machine, use the [Driver Configuration] dialog box. Make sure the settings match those of the PLC.

For an overview of the driver and protocol settings, see Section 5 – *Environment Setup*.



For information on how to display the [Driver Configuration] dialog box, see the online help.

COM Port	COM1 💌	Parity Bit	Odd
Serial Interface	RS-232C 💌	Stop Bit	1
Flow Control	None	Data Length	8
Transmission Speed	19200 💌	Rov. Time Out	5 · Sec
Retry Count	2 *	TX Wait Time	0 ≛ mSec

Manufacturer

Displays the name of the PLC manufacturer.

Interface

Displays the type of serial connection used to connect the target machine to the PLC.

COM Port

Defines which COM port to use on the target machine, for connecting to the PLC.

MEMO

Select COM1 for PS Series Type G target machines. Connection is not possible using COM2.

Serial Interface

Defines the serial connection: RS-232C or RS-422 (4-wire).

For details about the supported connections, see Section 2 – *Cable Diagrams*.

Flow Control

Defines the signals that control the data flow.

Transmission Speed

Sets the communication speed in bits per second. This setting must match the PLC baud rate.

Retry Count

Defines the number of times the driver tries to send or receive data when there is an error.

Parity Bit

Sets the parity bit for detecting communication errors.

Stop Bit

Defines the stop bit.

Data Length

Defines the length of each unit of data.

Rcv. Timeout

Defines the length of time the target machine waits for a response before it outputs a timeout error or sends another communication.

TX Wait Time

Defines the number of milliseconds that the target machine waits, after receiving a communication packet, before sending a response.

8 Device Address Configuration

To set up a PLC variable in the Variable List, use the Device Address Keypad from the variable properties.

See Section 3 – Supported Device Addresses.

MEMO

For information on how to display the Device Address Keypad, see the online help.

Programming Port Melsec-Q							
Device D							
Address							
0000 Clear							
7 8 9 E F							
4	5	6	С	D			
1	2	3	A,	в			
0 : Del BackSpace							
OK Cancel Help							
	nmii ice ress D000 7 4 1 0 4	nming P ice D ress - 00000 - 7 8 4 5 1 2 0 :	Numing Port Me ice D ress	Numing Port Melsec- ice 7 8 9 E 4 5 6 C 1 2 3 A 0 : Del Backs	Numing Port Melsec-Q ice ress 7 8 9 E 7 8 9 E F 4 5 6 C D 1 2 3 A B 0 : Del BackSpace Cancel Help		

Device

Lists the PLC's discrete and word device types.

Address

Enter the device address for the PLC variable. The keypad ensures that you enter the correct format for bit and word devices.